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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/801,603	03/08/2001	George Henry Ahrens	AUS9-2000-0923-US1	5314

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Joseph T. Van Leeuwen
P.O. Box 81641
Austin, TX 78708-1641

EXAMINER

WILSON, YOLANDA L

ART UNIT	PAPER NUMBER
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2184

DATE MAILED: 10/01/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/801,603

Applicant(s)

AHRENS ET AL.

Examiner

Yolanda Wilson

Art Unit

2184

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 March 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5, 7-10, 13-18 and 20 is/are rejected.
- 7) ☒ Claim(s) 6, 11, 12 and 19 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other: _____

DETAILED ACTION

Claim Objections

1. Claims 6,11,12,19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-5,7-10,13-18,20 rejected under 35 U.S.C. 102(e) as being anticipated by Austen et al. (US Publication Number 20020124215A1).

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

4. As appears in claim 1, Austen et al. discloses storing error data corresponding to each hardware platform error in a storage area, wherein the error data includes a flag

indicating whether the error the error data has been reported and reporting the error data on page 2, paragraphs 0015,0018, "the hypervisor 22 keeps track of which logic partition 12 has read which error logs by maintaining a list called a partition log matrix (PLM) 24 in NVRAM 16... The hypervisor 22 indicates the read status of a new error log in the PLM 24 by marking the entry as 'unread by my partition' in the records 26 for each active logical partition 12, and by marking entry as 'read by my partition' in the records 26 for each inactive logical partition 12 in step 56."

5. As per claims 2,9, and 15, Austen et al. discloses receiving a request from one of the partitions on page 1, paragraph 0005, "An event scan routine is instantiated for each of the logical partitions that make request to read new error logs in the error log partition." Austen et al. discloses providing the error data to the requesting partition, setting the flags included in one or more of the provided error data indicating that at least one partition has been notified of the error on page 2, paragraph 0019, "If the PLM 24 indicates that all of the error logs have been read by the corresponding logical partition 12 (i.e., marked as 'read by my partition') then the hypervisor 22 indicates such to the event scan routine."

6. As per claims 3 and 16, Austen et al. discloses the error data is stored in an error buffer on page 2, paragraph 0015, "the hypervisor 22 keeps track of which logic partition 12 has read which error logs by maintaining a list called a partition log matrix (PLM) 24 in NVRAM 16."

7. As per claims 4,10, and 17, Austen et al. discloses receiving the error data at one of the partitions, determining whether the errors included in the error data have

previously been reported to at least one partition, the determination including analyzing the flag corresponding to each error on page 2, paragraph 0019, "If the PLM 24 indicates that all of the error logs have been read by the corresponding logical partition 12 (i.e., marked as 'read by my partition') then the hypervisor 22 indicates such to the event scan routine."

8. As per claims 5 and 18, Austen et al. discloses transmitting one or more errors included in the error data in response to the determination being that the one or more errors have not previously been reported on page 1, paragraph 0005, "An event scan routine is instantiated for each of the logical partitions that make request to read new error logs in the error log partition."

9. As per claim 7, Austen et al. discloses receiving the hardware platform error from a component included in the hardware platform on page 1, paragraph 0011, "The control service processor 14 monitors the logical partitions 12 and reports errors that occur within the system 10. The CSP 14 writes reportable error logs into an error log partition 18 stored in the NVRAM 16." Austen et al. discloses setting the flag corresponding to the hardware platform error to indicate that the error has not been provided to at least one of the partitions, determining an error identifier that corresponds to the error, writing the error identifier and the corresponding flag to an error buffer on page 2, paragraph 0015, "the hypervisor 22 keeps track of which logic partition 12 has read which error logs by maintaining a list called a partition log matrix (PLM) 24 in NVRAM 16. The PLM 24 includes a record 26 for each logical partition 12 that includes

a field 28 for each error log in the error log partition 18 indicating whether or not the logical partition 12 has read the error log.”

10. As per claim 8, Austen et al. discloses one or more processor in Figure 1, label 12a. Austen et al. discloses one or more nonvolatile storage devices accessible by the processors, the nonvolatile storage devices including one or more system partitions in Figure 1, label 12b. Austen et al. discloses an error buffer for storing errors detected in the information handling system on page 1, paragraph 0011, “The CSP 14 writes reportable error logs into an error log partition 18 stored in the NVRAM 16.”

Austen et al. discloses a error handling tool to manage the detected error, the error handling tool including means for identifying one or more errors corresponding to the information handling system, means for including error data for each of the errors in the error buffer, wherein the error data includes a flag corresponding to each error indicating whether at least one system partition has been notified of the error on page 2, paragraph 0015, “the hypervisor 22 keeps track of which logic partition 12 has read which error logs by maintaining a list called a partition log matrix (PLM) 24 in NVRAM 16. The PLM 24 includes a record 26 for each logical partition 12 that includes a field 28 for each error log in the error log partition 18 indicating whether or not the logical partition 12 has read the error log.”

11. As per claims 13 and 20, Austen et al. discloses one or more components accessible by the processor on page 1, paragraph 0010, “The system 10 includes multiple logical partitions 12, a control service processor 14, and a nonvolatile random access memory (NVRAM) 16. Each logical partition 12 has its own processor(s) 12a,

memory 12b, and I/O devices 12c, and may run it won operating system 12d independently of the other logical partitions 12.”

Austin et al. discloses means for receiving the hardware platform error from a component included in the hardware platform on page 1, paragraph 0011, “The control service processor 14 monitors the logical partitions 12 and reports errors that occur within the system 10. The CSP 14 writes reportable error logs into an error log partition 18 stored in the NVRAM 16.” Austen et al. discloses means setting the flag corresponding to the hardware platform error to indicate that the error has not been provided to at least one of the partitions, means for determining an error identifier that corresponds to the error, means for writing the error identifier and the corresponding flag to an error buffer on page 2, paragraph 0015, “the hypervisor 22 keeps track of which logic partition 12 has read which error logs by maintaining a list called a partition log matrix (PLM) 24 in NVRAM 16. The PLM 24 includes a record 26 for each logical partition 12 that includes a field 28 for each error log in the error log partition 18 indicating whether or not the logical partition 12 has read the error log.”


12. As per claim 14, Austen et al. discloses means for identifying one or more errors corresponding to the hardware platform on page 1, paragraph 0011, “The control service processor 14 monitors the logical partitions 12 and reports errors that occur within the system 10. The CSP 14 writes reportable error logs into an error log partition 18 stored in the NVRAM 16.” Austen et al. discloses means for including error data for each of the errors in a storage area, wherein the error data includes a flag corresponding to each error indicating whether at least one partition has been notified of

the error on page 2, paragraph 0015, "the hypervisor 22 keeps track of which logic partition 12 has read which error logs by maintaining a list called a partition log matrix (PLM) 24 in NVRAM 16. The PLM 24 includes a record 26 for each logical partition 12 that includes a field 28 for each error log in the error log partition 18 indicating whether or not the logical partition 12 has read the error log."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yolanda Wilson whose telephone number is (703) 305-3298. The examiner can normally be reached on M-F (7:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (703) 305-9713. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.


ROBERT BEAUSOLIEL
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100